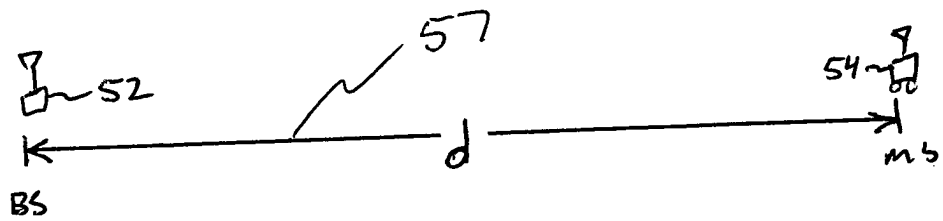


FIG. 1

75



$$\text{Estimated chip delay} \cong \frac{d}{\text{speed of light}}$$

FIG. 2

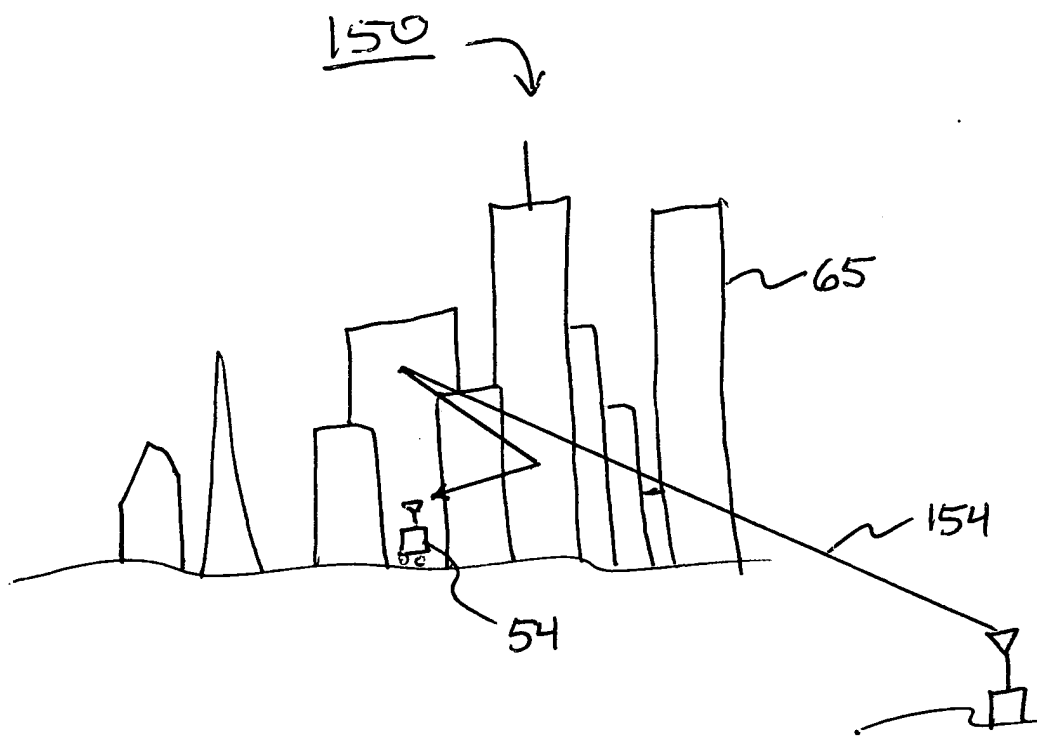


FIG. 3

200 →

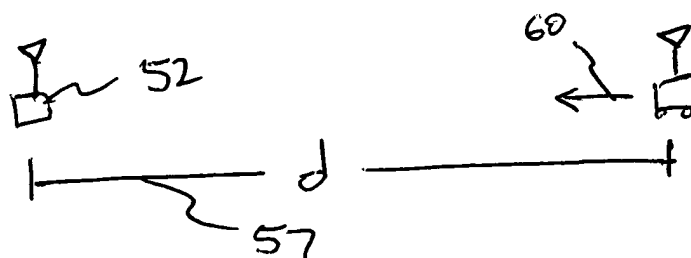


FIG. 4

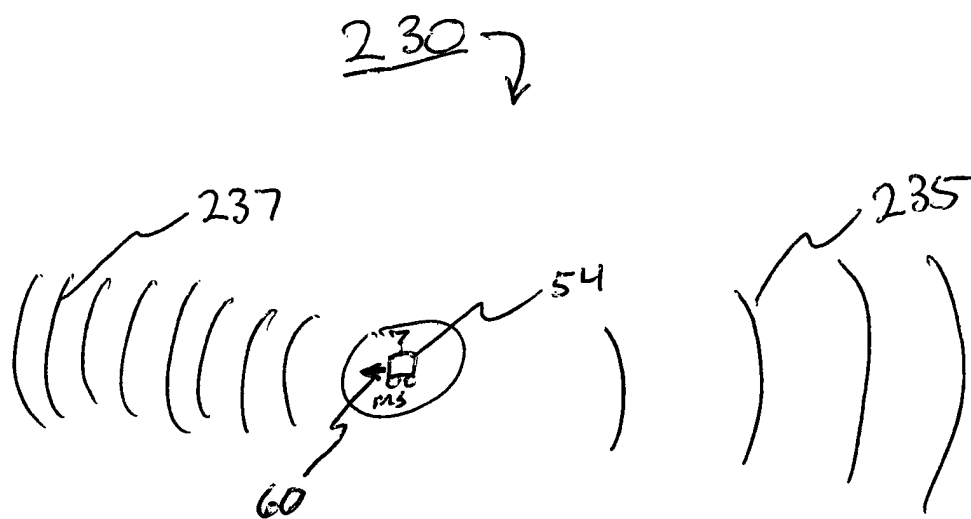


FIG. 5

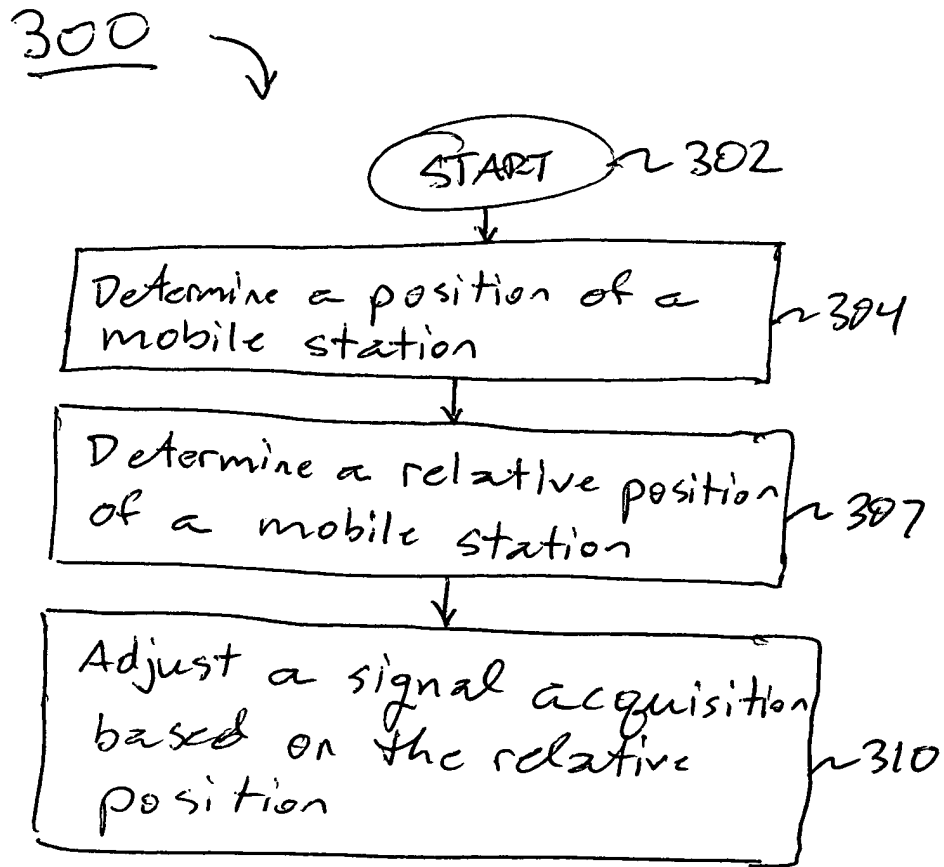


FIG. 6

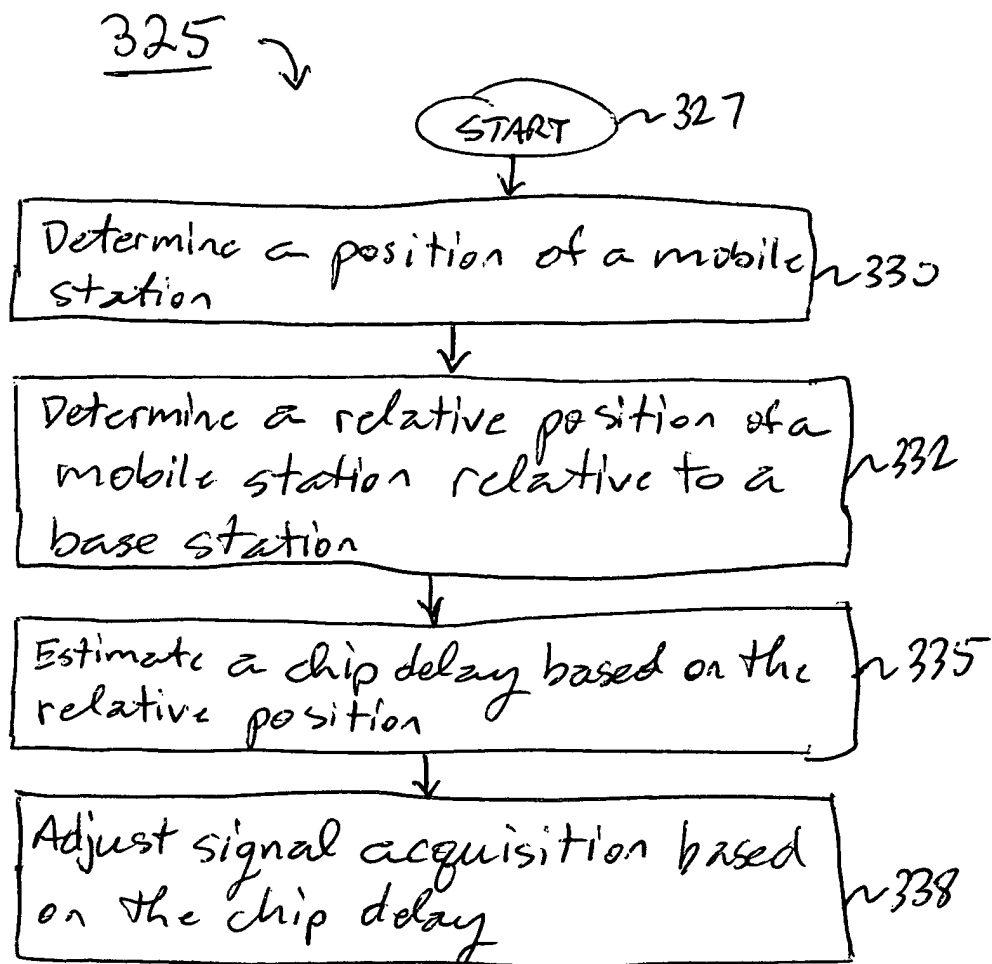


FIG. 7

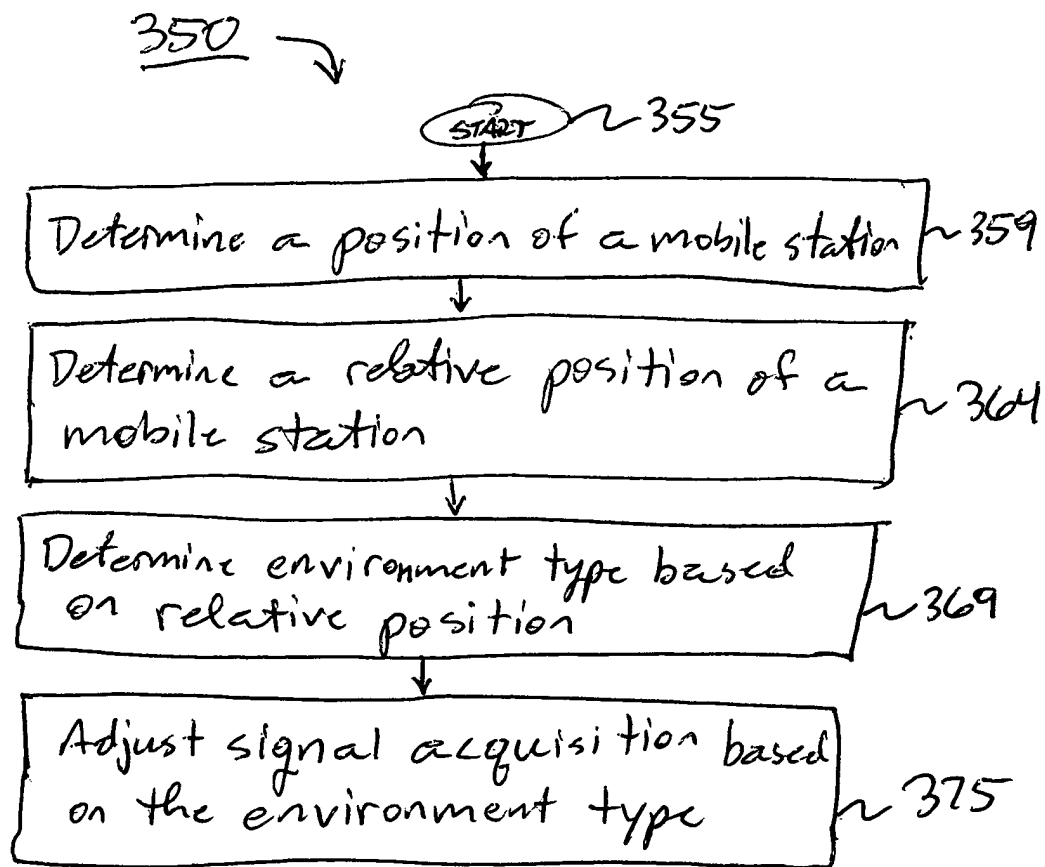


FIG. 8

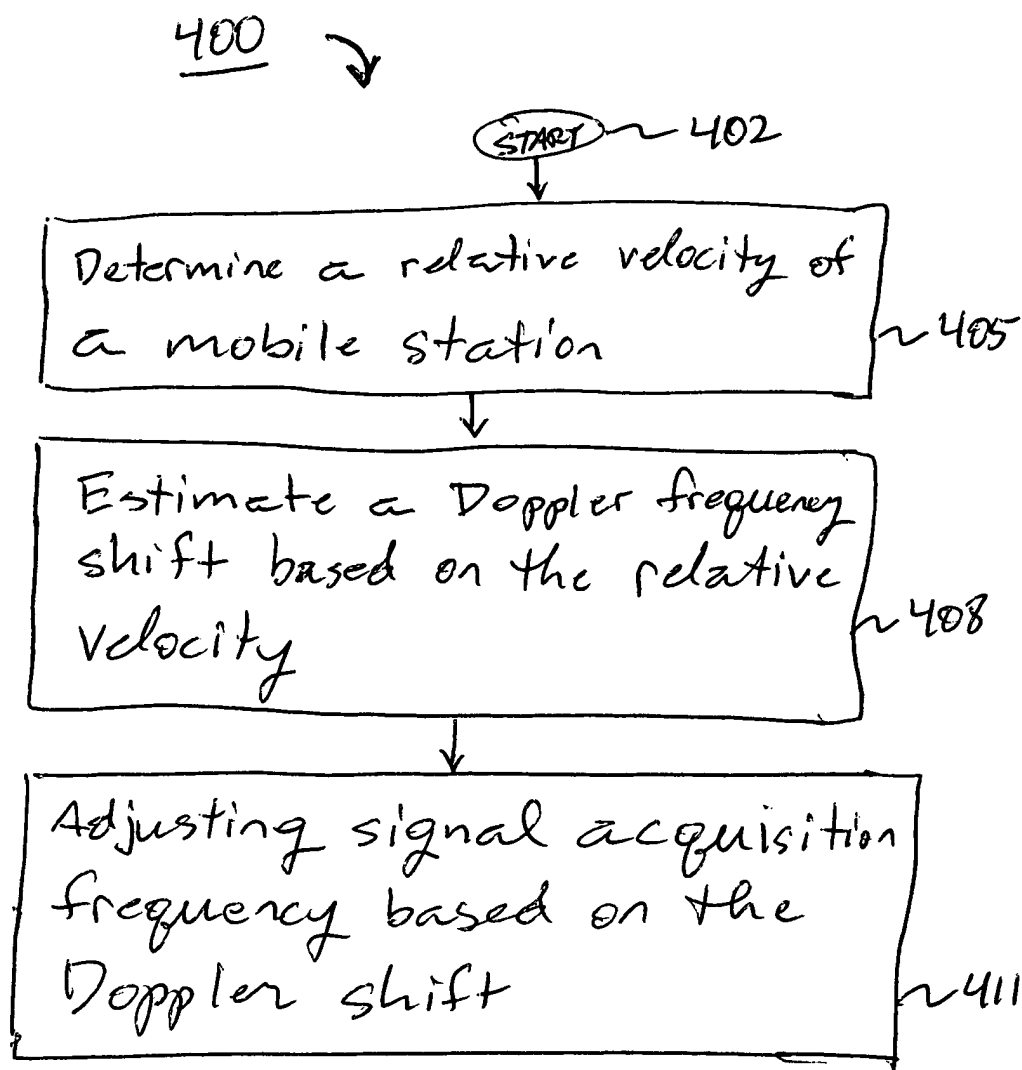


FIG. 9

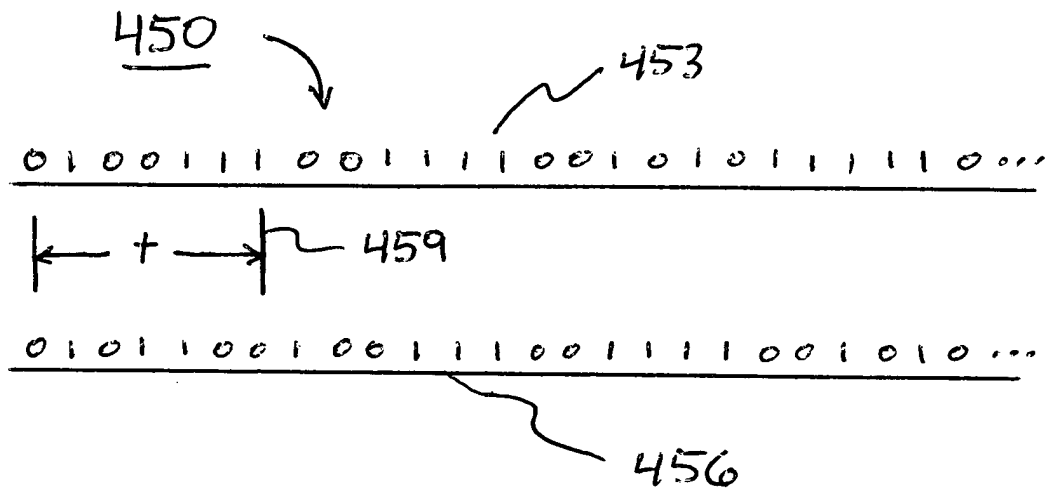


FIG. 10

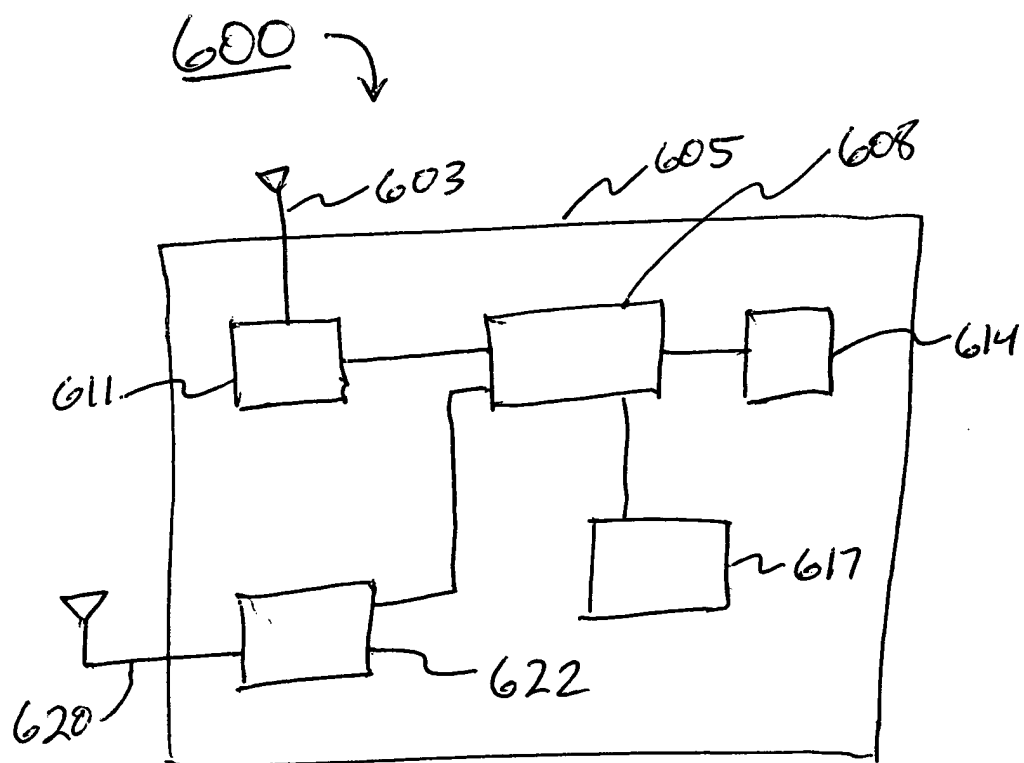


FIG. 11